

Topics of Interest (but not limited to...)

- ❖ Semiconductor Devices
- ❖ Vacuum Tube Devices
- ❖ VLSI Design & VLSI Technology
- ❖ Nanoelectronics
- ❖ Optoelectronics Devices
- ❖ MEMS
- ❖ Analog Circuits & Digital Circuits
- ❖ Embedded Systems

Registration Fees

The participation is open to all interested faculty including students of M.Tech and B.Tech (3rd and 4th Year). The Registration fee includes the conference materials, working lunch and snacks.

Research Scholars/ Students : INR 300/-

Academic Staff/ Faculties : INR 500/-

Industrial Personnel : INR 2500/-

Payment Details

Send the Demand Draft in favor of IEEE EDS-NIST Student Chapter payable at Andhra Bank, Gosani Nuagaon, Berhampur (IFSC Code: ANDB0000172, MICR Code: 760011006, Branch Code: 000172). For NEFT: 017211100001255, Name: M/S IEEE EDS CHAPTER

Address For Communication

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Organizing Committee

Patrons

Prof. Sangram Mudali

Director, NIST

Prof. Geetika Mudali

Placement Director, NIST

Convener and Program Chair

Prof. (Dr.) Ajit Kumar Panda

Organizing Secretary

Prof. M Suresh

Joint-Organizing Secretary

Prof. Mukesh Kumar Sukla

Important Dates

Last Date of Paper Submission : 20th Feb, 2017

Notification of Acceptance : 22nd Feb, 2017

Last Date for Registration : 22nd Feb, 2017

Members

Dr. Trinath Sahu

Dr. Satya Sopan Mahato

Dr. Palash Dash

Dr. Debasish Panda

Dr. Sandipan Malik

Prof. Harikrushna Gantayat

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NCDC - 2017



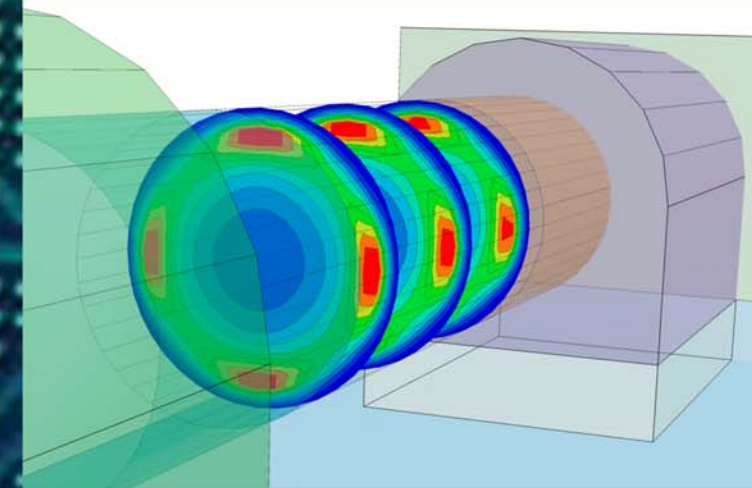
3rd National Conference

on

“Devices and Circuits”

by

IEEE EDS NIST STUDENT CHAPTER



7th March 2017

Organized by



NATIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY
Palur Hills, Berhampur-761008, Odisha, India

About The Conference

National Conference on Device and Conference (NCDC) is a flagship event of IEEE EDS-NIST Student Chapter of Region-10. This conference is two year old and organized successfully last year. The success can be measured from 96 participants and 33 accepted papers. All the accepted papers have been published in the proceedings of 1ST National Conference on Devices and Circuits with ISBN Code 978-93-82208-78-5. This conference also includes technical talks by eminent professors from IITs and top ranked universities to create an awareness of emerging technologies and ongoing research in VLSI Circuit design and Devices.

IEEE EDS-NIST Student Chapter and ECE Department

The IEEE-EDS student chapter at National Institute of Science & Technology (NIST), Berhampur is an active chapter and conducts different students activities relating to Electron Devices and conducts IEEE sponsored Mini – Colloquium every year. This is an opportunity to grow the VLSI fundamentals in Odisha. Many activities like MQ, Conference, Technical Quiz, Technical Lecture Series, Workshops and Sort-Term Training Programs are being conducted round the year at suitable time for faculties, research scholars and Engineering graduates. Students have received the Cadence Design Contest award in 2014 and 2014, 2015 & 2016 consecutively. Also in 2016 in of our project was in top 5 under PG level.

About NIST

The National Institute of Science and Technology, (NIST) was established in 1996, NAAC accredited with Grade "A" with a high score of 3.22 out of 4 at Berhampur, Odisha. This institute is being managed by the SM Charitable Educational Trust with the aim

to create Engineering minds capable of mastering the global challenges of tomorrow's technology by promoting higher technical education. It is approved by AICTE and affiliated to BPUT. The institute is laid by Prof. Sangram Mudali (M.S., University of Houston), Founder Director and Prof. Geetika Mudali (M.S. New Jersey Institute of Technology), Placement Director. About 3200 students are studying in various disciplines of M.Tech., B.Tech., MCA, MBA at NIST. This young organization has produced 4 BOYSCAST scholars, 2 Full Bright scholars, 3 Samant Chandrashekhar Young Scientist Awardees and more than 20 Post-Doctorate scholarships have been awarded to the institute faculty. The institute received a number of grants from AICTE, DST, UGC, MHRD, CSIR and private IT companies for projects, Staff and Faculty Development Programmes. NIST has made a footprint at state level and national level by receiving many grants from DST, AICTE. NIST has established TIFACCORE Center of Excellence in "3G/4G Communication Technologies" with a grant of about Rs.10Cores sponsored by DST & Industry Partners.

Call for Papers

- ☞ Researchers, academicians and professionals interested to present papers at NCDC-2017 are invited to submit full length papers not exceeding four A4 size pages.
- ☞ Soft copies of the papers should be submitted as a PDF file as per the IEEE conference paper format (two columns).
- ☞ Authors of the accepted papers will be informed by email.
- ☞ It is mandatory for at least one of the authors to register in non-student category for publication of the paper in proceedings.

Registration Form For NCDC - 2017

IEEE ED-NIST Student Chapter

National Institute of Science and Technology

Palur Hills, Berhampur, Odisha.

Date: 7TH March, 2017

NAME:

Designation:

Institute Name and Address:

.....

Contact Number:

EMail:

IEEE Membership ID (If Available):

Accommodation (Yes/ No):

Do you need accommodation? [YES/ NO]

Registration Category (Tick the Appropriate One):

[a] Research Scholar/ Student

[b] Academician

[c] Industrial Personnel

Payment:

DD/ NEFT Details:

Drawing Bank Details:

Date of Issue:

Date:

Signature

*** *All the participants are requested to bring the organization*

Identy Card as a proof for registration category.