ICT BASED FACULTY DEVELOPMENT PROGRAMME ON "Embedded System"

FROM 19TH TO 23RD MARCH, 2018

OP NO: ICT51

REGISTRATION FORM

Name:…………………………………………………

Designation:……………………………………………

Organization:……………………………………………

Address:…………………………………………………

…………………………………………………………

Contact Number:…………………………………………

E-mail id:…………………………………………………

Date:

Place: **Signature of Applicant**

Registration :

Registration is free of cost.

Registration will be on first come first serve basis.

Accommodation will be available will first come first serve basis

No TA & DA will be provided to the participants.

Important Dates

On spot registration will start from 7.00 AM onwards on 19th March, 2018.

Confirmation of participants on or before 17th March 2018

Only 25 Faculty can participate on first come first serve basis.

Accommodation

Limited accommodation is available at GIET Campus hostel for interested outstation participants.

Contact Persons (for any related query)

Convener:
Prof. Nalini Kumar Sethy
Contact Number: +91-7682830034

Co-Convener:
Prof. Samarendra Samal
Contact Number : +91-7682830032

Email: hod_cse@gietbbsr.edu.in

In Association with

National Institute of Technical Teachers Training & Research, Chandigarh

Organized by

Department of Computer Science & Engineering

And

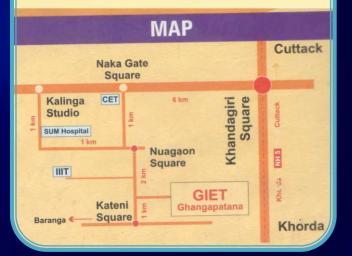Department of Electronics & Communication Engineering

GANDHI INSTITUTE OF EXCELLENT TECHNOCRATS

GHANGAPATNA, BHUBANESWAR ,ODISHA

(Approved by AICTE & Govt. of Odisha ,Affiliated to BPUT)

About GIET, GHANGAPATNA

Gandhi Institute of Excellent technocrats, a Gandhi Group of Institution is located in temple city of Bhubaneswar, established in the year 2009. It is approved by AICTE, New Delhi and affiliated to BPUT, Odisha. This section of Gandhi envisaged being the center of excellence in engineering education which spreads over 20 acres of campus in the serene suburbs of Ghangapatna. The college understands the parents pay their hard earned money and students burn midnight oil to achieve a goal in order to secure a decent employment after completion of studies. The college offers B-Tech programmes in ME, CE, CSE, EE, EEE, ECE etc.

How to reach

Scope & Objective

The objective of the course is to focus on Embedded System Designs, the latest trend including Micro Controllers overview & Embedded Systems applications.

Highlights:

A Seminar on 5 days based on the above for the integrated development of the skills and knowledge for the faculty members.

- Introduction to micro-controllers like PIC, AVR, Embedded Processors.
- Reconfigurable devices like MMX series, MIPS, ARM system on chip.
- Applications specific integrated circuits.
- Modeling of multi processors system, applications of Embedded system designing includes wireless sensor and their networking.

Eligibility:

All technical faculty members are eligible.

Resource Person:
Eminent professors from NITTTR will deliver talks on Embedded System through Video Conference.

Chief Patrons:
Prof. (Dr.) Satya Prakash Panda, Chairman
Er. Rama Narayan Sabat, Vice Chairman

Patrons:
Prof. (Dr.) Rajendra Kumar Das, Principal
Prof. Satya Prakash Das, Vice Prinicipal

Convener:
Prof. Nalini Kumar Sethy, HOD CSE

Co-Convener:
Prof. Samarendra Samal, HOD, ECE

Co-ordinator:
Prof. Srinivas Mishra, CSE

Advisory Committee:
Prof. (Dr.)Surendra Kumar Yadav, Univ. of Delhi
Prof. (Dr.)D.A. Agunbide,Univ. of Johansberg
Prof. (Dr.)Saroj Kumar Padhan, VSSUT, Burla
Prof. (Dr.)Chandan Kumar Sahoo, HOD BSE
Prof. Jitendra Padhi, HOD ME
Prof. Tarakanta Sahoo, HOD HUM
Prof. BTM Nayak, HOD EE&EEE
Prof. Ganeswar Sahu, HOD CE
Mr. Hemo Chandra Jena, DGM
Mr. Utkal Ranjan Das, Registrar
Mr. Jatindra Kumar Mishra. AO(A)
Mrs. Sumita Padhi, Director Placement

Organizing Committee:
- Prof. Manorama Subudhi
- Prof. Sriram Pradhan
- Prof. Biswaranjan Behera
- Prof. Rajani Kanta Sahu
- Prof. Debasish Pradhan
- Prof. Mandakini P. Behera
- Mr. Satprem Tripathy
- Mr. Sadhu Charan Moharana
- Mr. Siva Sankar Sahu